

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Derrick Chu Lin, et al.

Examiner: Not yet assigned

Application No.: Not yet assigned

Art Unit: Not yet assigned

Filed: July 18, 2003

For: A PROCESSOR CAPABLE OF
EXECUTING PACKED SHIFT
OPERATIONS

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with copies of the documents cited on that form. It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

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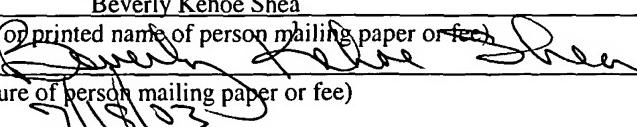
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Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

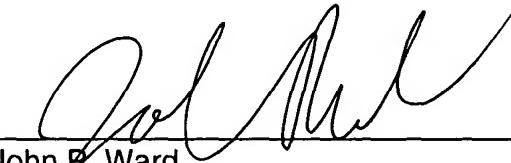
- X 37 C.F.R. §1.97(b).
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- A statement pursuant to 37 C.F.R. §1.97(e) or
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- (1) A statement pursuant to 37 C.F.R. §1.97(e);
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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: July 18, 2003


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Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)		Attorney Docket No.: 42390.P3465XD	Application Number: Not yet assigned
Sheet 1 of 3		First Named Inventor: Derrick Chu Lin, et al.	Examiner: Not yet assigned
		Filing Date: July 18, 2003	Art Unit: Not yet assigned

U.S. PATENT DOCUMENTS

Exam. Initial*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (If known)			
		3,711,692		Batcher	01-16-1973	
		3,723,715		Chen et al.	03-27-1973	
		4,139,899		Tulpule et al.	02-13-1979	
		4,161,784		Cushing et al.	07-17-1979	
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		4,707,800		Montrone et al.	11-17-1987	
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		5,481,746		Schiffleger et al	01-02-1996	

Examiner Signature		Date Considered	
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¹Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document, by the two-letter code (WIPO Standard S.3). ⁴For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)	Attorney Docket No.: 42390.P3465XD	Application Number: Not yet assigned
Sheet 2 of 3	First Named Inventor: Derrick Chu Lin, et al.	Examiner: Not yet assigned
	Filing Date: July 18, 2003	Art Unit: Not yet assigned

U.S. PATENT DOCUMENTS

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Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)		Attorney Docket No.: 42390.P3465XD	Application Number: Not yet assigned
Sheet 3 of 3		First Named Inventor: Derrick Chu Lin, et al.	Examiner: Not yet assigned
		Filing Date: July 18, 2003	Art Unit: Not yet assigned

OTHER ART - NO PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Translation ²
		CASE, B., "Philips Hopes to Displace DSPs with VLIW, TriMedia Processors Aimed at Future Multimedia Embedded Apps," Microprocessor Report, Dec. 1994, pp. 12-18.	✓
		Errata to MC88110 Second Generation RISC Microprocessor User's Manual, Motorola, Inc., 1992, pp. 1-11.	✓
		GWENNAP, L., "New PA-RISC Processor Decodes MPEG Video, H"'s PA-7100LC Uses New Instructions to Eliminate Decoder Chip," Microprocessor Report, Jan. 1994, pp. 16-17.	✓
		i860 TM. Microprocessor Family Programmer's Reference Manual, Intel Corporation, 1992, Chapters 1, 3, 8, and 12.	✓
		Intel i750, i860 TM, i960 Processors and Related Products, 1993, pp. 1-3.	✓
		KAWAKAMI, Y., et al., "A Single-Chip Digital Signal Processor for Voiceband Applications," IEEE, 1980 International Solid-State Circuits Conference, pp. 40-41.	✓
		LEE, R.B., "Accelerating Multimedia with Enhanced Microprocessors," IEEE Micro, April 1995, pp. 22-32.	✓
		MARGULIS, N., "i860 Microprocessor Architecture," McGraw Hill, Inc., 1990, Chapters 6, 7, 8, 10, and 11.	✓
		MC88110 Programmer's Reference Guide, Motorola, Inc., 1992, pp. 1-4.	✓
		MC88110 Second Generation-RISC Microprocessor User's Manual, Motorola, Inc., September 1992, pp. 1-1 through 2-23, pp. 2-1 through 2-20, 3-1 through 3-32, 5-1 through 5-25, 10-62 through 10-71, Index 1 through 17.	
		Motorola MC88110 Second Generation RISC Microprocessor User's Manual, Motorola, Inc., 1991.	
		Pentium Processor's User's Manual, Volume 3: Architecture and Programming Manual, Intel Corporation, 1993, Chapters 1, 3, 4, 6, 8, and 18.	
		SHIPNES, J., "Graphics Processing with the 88110 RISC Microprocessor," Motorola, Inc., IEEE, No. 0-8186-26455-0/92, 1992, pp. 169-174.	
		TMS320c2X, User's Guide, Digital Signal Processing Products, Texas Instruments, 1993, pp. 3-2 – 3-11; 3-28 – 3-34; 4-1 – 4-22; 4-41; 4-103; 4-119; 4-120; 4-122; 4-150; 4-151.	
		UltraSPARC Multimedia Capabilities On-Chip Support for Real0-Time Video and Advanced Graphics; SPARC Technology Business, Sept. 1994, Sun Microsystems, Inc.	

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